PLUS Search Results for S/N 09834013, Searched October 18, 2004

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system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that a

most closely related linguistically to the application searched. search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

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09834013_LIST

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5949011
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09834013 CLS

Most Frequently Occurring Classifications of Patents Returned From A Search of 09834013 on October 18, 2004

Original Classifications 5 710/74 84/602 2 360/46 710/5 Cross-Reference Classifications 711/112 5 710/5 4 710/105 3 84/604 3 84/609 2 84/645 2 360/75 2 360/97.01 2 360/99.08 2 710/107 710/266 Combined Classifications 710/5 6 710/74 6 711/112 4 84/602 4 710/105 3 84/604 3 84/609 2 84/645 2 360/46 2 360/75 2 360/97.01 2 360/99.08 2 708/525 2 710/1 2 710/107 2 710/22 2 710/266 2 710/305 2 711/117 2 711/122 2 711/138

2

712/213 713/400

09834013 CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returne

From A Search of 09834013 on October 18, 2004

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7
  710/5
                (2 \text{ OR}, 5 \text{ XR})
                710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
        Class
                        PROCESSING SYSTEMS:
                                              INPUT/OUTPUT
        710/1
                      INPUT/OUTPUT DATA PROCESSING
        710/5
                      .Input/Output command process
   710/74
                 (5 OR, 1 XR)
        Class
                710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
                        PROCESSING SYSTEMS:
                                              INPUT/OUTPUT
        710/1
                      INPUT/OUTPUT DATA PROCESSING
        710/62
                      .Peripheral adapting
        710/72
                      .. Application-specific peripheral adapting
        710/74
                      ... For data storage device
                (0 OR, 6 XR)
   711/112
                711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
        Class
                        SYSTEMS: MEMORY
        711/100
                      STORAGE ACCESSING AND CONTROL
                      .Specific memory composition
        711/101
        711/111
                      .. Accessing dynamic storage device
        711/112
                      ...Direct access storage device (DASD)
    84/602
                (4 OR, 0 XR)
4
                084 : MUSIC
        Class
        84/1
                      INSTRUMENTS
        84/600
                      .Electrical musical tone generation
        84/601
                      ..Data storage
                      ... Digital memory circuit (e.g., RAM, ROM,
        84/602
                         etc.)
   710/105
                (0 OR, 4 XR)
                710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
        Class
                                              INPUT/OUTPUT
                        PROCESSING SYSTEMS:
        710/100
                      INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                          TRANSACTION PROCESSING)
        710/105
                      .Protocol
                (0 OR, 3 XR)
3
    84/604
        Class
                084 : MUSIC
        84/1
                      INSTRUMENTS
        84/600
                      .Electrical musical tone generation
        84/601
                      ..Data storage
        84/602
                      ... Digital memory circuit (e.g., RAM, ROM,
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09834013_CLSTITLES etc.)

....Waveform memory 84/604

3	84/609 (0 Class 08 84/1 84/600 84/601 84/602	34	: MUSIC
2	84/645 (Class 08 84/1 84/600 84/601 84/645	3 4	
2	360/46 (Class 360/39 360/46	60	OR, 0 XR) : DYNAMIC MAGNETIC INFORMATION STORAGE OR RETRIEVAL GENERAL PROCESSING OF A DIGITAL SIGNAL .Head amplifier circuit
2		60	OR, 2 XR) : DYNAMIC MAGNETIC INFORMATION STORAGE OR RETRIEVAL AUTOMATIC CONTROL OF A RECORDER MECHANISM .Controlling the head
2	360/88	50	OR, 2 XR) : DYNAMIC MAGNETIC INFORMATION STORAGE OR RETRIEVAL RECORD TRANSPORT WITH HEAD STATIONARY DURING TRANSDUCING .Disk record
2	360/88	60	: DYNAMIC MAGNETIC INFORMATION STORAGE OR RETRIEVAL RECORD TRANSPORT WITH HEAD STATIONARY DURING TRANSPUCING .Disk record
2	708/525 ((1	OR, 1 XR)

09834013 CLSTITLES

	Class 708/100 708/200 708/490 708/525		09834013_CLSTITLES : ELECTRICAL COMPUTERS: ARITHMETIC PROCESSING AND CALCULATING ELECTRICAL DIGITAL CALCULATING COMPUTER .Particular function performedArithmetical operationStatus condition/flag generation or use
2	710/1 Class 710/1		OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING
2	710/100	710	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING) .Bus access regulation
2		710	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING .Direct Memory Accessing (DMA)
2	710/260	710	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INTERRUPT PROCESSING .Programmable interrupt processing
2	710/305 Class 710/100 710/305	710	: ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
2	711/100	711	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY STORAGE ACCESSING AND CONTROL .Hierarchical memories
2	711/122 Class 711/100	711	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY STORAGE ACCESSING AND CONTROL

	711/11 711/11 711/11 711/11	18 19	09834013_CLSTITLES .Hierarchical memoriesCachingMultiple cachesHierarchical caches
2	711/138		
	Class	711	: ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
	711/1		STORAGE ACCESSING AND CONTROL
	711/1: 711/1:		<pre>.Hierarchical memoriesCaching</pre>
	711/1	38	Cache bypassing
2	712/213		
	Class	712	: ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS
TRUC	TION		
	712/20	09	PROCESSING .Decoding instruction to accommodate plural instruction interpretations (e.g., differe
nt dialects,			
	712/2	13	languages, emulation, etc.) .Predecoding of instruction component
2	713/400		
	Class	713	: ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT
	713/40	00	SYNCHRONIZATION OF CLOCK OR TIMING SIGNALS, DATA, OR PULSES